

# Abstracts

## Parallel Feedback FETDRO Design Using 3-Port S-Parameters

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*A.P.S. Khanna. "Parallel Feedback FETDRO Design Using 3-Port S-Parameters." 1984 MTT-S International Microwave Symposium Digest 84.1 (1984 [MWSYM]): 181-183.*

A new approach for the design of parallel feedback FET oscillators has been presented using 3-Port S parameters. The linear analysis accurately determines the reflection gain at any port as a function of the parameters of the dielectric resonator parallel feedback network between the other two ports of the FET. An example of design and practical realization of FETDRO is presented at 9 GHz.

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